

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Group Art Unit of parent application: 2823

Examiner of parent application: Thanh V Pham

In Re PATENT APPLICATION Of:

Applicants : Jiro MATSUMITO )

Appln. No. : To Be Assigned )

Div of Ser. No : 10/103,889 filed March 25, 2002 )

Filed : Herewith )

For : SEALING APPARATUS FOR )  
SEMICONDUCTOR WAFER, MOLD OF )  
SEALING APPARATUS AND )  
SEMICONDUCTOR WAFER (amended) )

Attorney Ref.: F00ED0150-DIV )

**PRELIMINARY  
AMENDMENT**

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December 5, 2003

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Preliminary to examination, please amend the application as follows: